

Post-Silicon And Runtime Verification For Modern Processors By Ilya Wagner;Valeria Bertacco

By Ilya Wagner;Valeria Bertacco

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Post-silicon validation Sanjit A. Seshia - in Proceedings of the International Conference on Runtime Verification (RV, 2012 verification, and <http://citeseerx.ist.psu.edu/showciting?cid=19317759>

The purpose of this book is to survey the state of the art and evolving directions in post-silicon and runtime verification. The authors start by giving an overview <http://www.bol.com/nl/p/post-silicon-and-runtime-verification-for-modern-processors/9200000035942920/>

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<http://chipdesignmag.com/display.php?articleId=3404>

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Post-Silicon and Runtime Verification for Modern Processors by Ilya Wagner, Valeria Bertacco English | 2010 | ISBN: 1441980334 | 224 pages | PDF | 5.6 MB

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